

ATTORNEY DOCKET NO. 114596-031-000127BS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 09/672,424 Confirmation No.: 7407  
Applicant: Gurjeet S. Saund, et al.  
Title: COMPLEX INSTRUCTION SET COMPUTER  
Filed: September 28, 2000  
Art Unit: 2183  
Examiner: Eric Coleman

Atty. Docket: 114596-031-000127BS  
Customer No. 38492

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make of record the items listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to fully consider the items and independently ascertain their teaching before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

1. Applicant has listed publication dates on the attached Form PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated. Applicant reserves the right to establish the patentability of the claims over any information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

2. For items, other than U.S. patents, listed on the enclosed Form PTO-1449 for which a copy is not already made of record in this application, a copy was previously cited by or submitted to the Patent and Trademark Office in application Serial No. 09/239,194 or 09/322,443, in "NPL Documents" entries in the IFW on the dates and having the number of pages indicated:

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on September 27, 2005 as First Class Mail in an envelope with sufficient postage addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



	09/239,194	09/322,443
Nanodata Corporation, QM-1 Hardware level User's Manual (1975)	5/6/03 255	
Nanodata Corporation, QM-2 Processor (1976)	5/6/03 31	
R.E. Hank, et al., Proceeding of the 28 <sup>th</sup> Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)		11/26/99 20*
Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, ACM Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)	5/6/03 18	

\* The 20-page "NPL Documents" of 11/26/1999 combines two papers, one by Cox, and the one by Hank that is listed on the attached IDS.

3. This Information Disclosure Statement is being filed at a time when a nominally-final Office Action has been mailed, but when finality of that Office Action should be withdrawn, or when prosecution may be reopened on withdrawal or reversal of all pending rejections. In the event of reopening of prosecution, entry of this Information Disclosure Statement is proper on payment of a fee pursuant to 37 C.F.R. §1.97(c)(2). Upon such entry, charge the fee due under C.F.R. §1.17(i)(1) to Deposit Account No. 23-2405, Order No. 114596-031-000127BS.

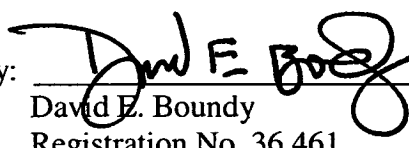
4. The Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to Deposit Account No. 23-2405, Order No. 114596-031-000127BS.

Respectfully submitted,

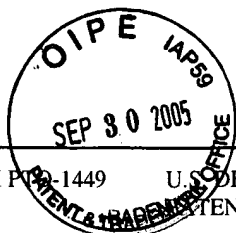
WILLKIE FARR & GALLAGHER LLP

Dated: September 27, 2005

By: \_\_\_\_\_

  
David E. Boundy  
Registration No. 36,461

WILLKIE FARR & GALLAGHER LLP  
787 Seventh Ave.  
New York, New York 10019  
(212) 728-8757  
(212) 728-9757 Fax



FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE <b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)				ATTY. DOCKET NO. 114596-031-000127BS		SERIAL NO. 09/672,424	
				APPLICANT Gurjeet S. Saund, et al.			
				FILING DATE September 28, 2000		GROUP ART UNIT 2183	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,934,832	8/23/2005	Van Dyke	712	244	Sep. 21, 2000
		6,941,545	9/6/2005	Reese			May 28, 1999
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES      NO
<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Papers, Etc.)</i>							
		R.E. Hank, et al., "Region-Based Compilation: An Introduction and Motivation" Proceedings of the 28th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)					
		Nanodata Corporation, QM-1 Hardware level User's Manual (1975)					
		Nanodata Corporation, QM-2 Processor (1976)					
		Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, ACM Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)					
<b>EXAMINER</b>				<b>DATE CONSIDERED</b>			